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[GB/GB]; c/o Aspex Semiconductor Limited, York House, Cottingley Business Park, Bradford, BD16 1PF (GB).

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(74) Agents: AHMAD, Sheikh, Shakeel et al.; David Keltie Associates, Fleet Place House, 2 Fleet Place, London EC4M 7ET (GB).

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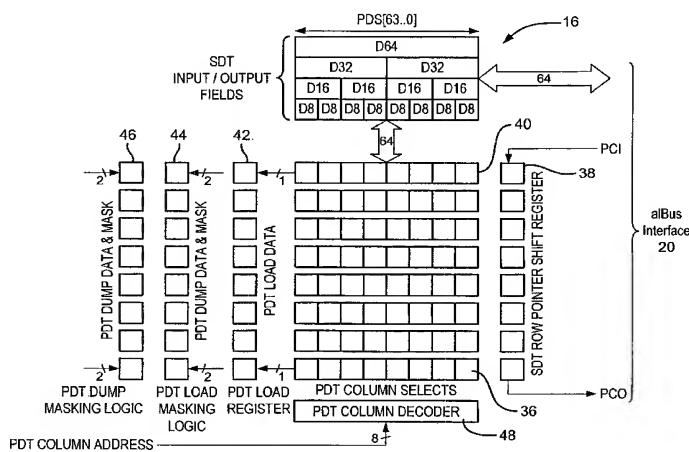
(71) Applicant (for all designated States except US): AS-PEX SEMICONDUCTOR LIMITED [GB/GB]; Rapid House, 40 Oxford Road, High Wycombe, Buckinghamshire, HP 11 2EE (GB).

(72) Inventors; and

(75) Inventors/Applicants (for US only): JALOWIECKI, Ian [GB/GB]; c/o Aspex Semiconductor Limited, York House, Cottingley Business Park, Bradford, BD16 1PF (GB). WHITAKER, Martin [GB/GB]; c/o Aspex Semiconductor Limited, York House, Cottingley Business Park, Bradford, BD16 1PF (GB). BOUGHTON, Donald

[Continued on next page]

(54) Title: IMPROVEMENTS RELATING TO ORTHOGONAL DATA MEMORY



(57) Abstract: A multi-ported orthogonal data memory (16) for effecting a corner-turning function, where for example data input as a sequence of bit-parallel word-serial data transfers are converted to data output in a bit-serial, word-parallel fashion, is described. The memory (16) is arranged to transfer data words comprising a plurality of data items and comprising: a plurality of data memory cells (36) arranged in the form of a matrix having rows and columns, and a plurality of groups (A, B, C, D) of memory cells (36) within the matrix, each group (A, B, C, D) being defined across multiple rows and columns and being individually addressable to effect transfer of a data word thereto; and enabling means having dedicated strobe connections (PDTEN) to each of the plurality of groups (A, B, C, D) of memory cells (36) and being arranged to enable selected ones of the plurality of groups (A, B, C, D) of memory cells (36) to read data present at their inputs, or to write stored data to their outputs, in a single transfer operation.

WO 2005/088640 A3



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